

DIGITAL ELECTROCHROMIC MIRROR SYSTEM

CROSS-REFERENCE TO RELATED APPLICATIONS

This application is a continuation-in-part of application Serial No. 08/832,380 filed on April 2, 1997, by Kenneth L. Schierbeek, the disclosure of which is hereby incorporated herein by reference, and further claims priority from provisional patent applications Serial 5 No. 60/103,721 filed on October 9, 1998, by Kenneth Schofield and Kenneth L. Schierbeek, and Serial No. 60/117,633 filed on January 28, 1999, by Kenneth Schofield and Kenneth L. Schierbeek, the disclosures of which are hereby incorporated herein by reference.

BACKGROUND OF THE INVENTION

10 This invention relates generally to vehicle rearview mirror systems and, more particularly, to electrochromic mirror systems, including an interior electrochromic rearview mirror assembly and at least one exterior electrochromic rearview mirror assembly.

An electrochromic rearview mirror assembly includes an electrochromic reflective element that responds to a signal applied across a pair of terminals by varying the light 15 reflected by the reflective element. In this manner, the reflectance level of the reflective element can be varied by varying the signal applied to the electrochromic cell. The electrochromic reflective element has characteristics which makes control of its reflectance level difficult. The electrochromic reflective element operates at a relatively low voltage, which typically may not exceed approximately 2 volts DC, more typically not more than 20 about 1.5 volts DC, for more than a brief period of time or else useful life of the reflective element is compromised. Furthermore, the amount of voltage necessary to establish a particular value of the light transmissivity of the cell varies with the temperature of the cell, the amount of change in light transmittance undertaken, and the surface area of the cell. Therefore, optimum control of the electrochromic reflective element may require more than 25 merely applying a DC voltage corresponding to the desired reflectance level.

Electrochromic mirror systems have incorporated therein many additional electronic functions in addition to mirror drive. These include compass functions; namely, 30 determination and display of vehicle heading, display of the interior and/or exterior temperature as well as other functions as disclosed in commonly assigned United States Patent No. 5,798,575, the disclosure of which is hereby incorporated herein by reference. By combining multiple functions in a single system, it is possible to utilize the computational power of a microcomputer in order to perform a wide range of functions. Although a microcomputer has extensive computational capability, its ability to supply a drive signal to

external components, such as an electrochromic cell, is limited. Therefore, it has been necessary to provide an interface circuit between the microprocessor and the electrochromic cell in order to supply the electrochromic cell with a desired reflectance level established by the microcomputer. United States Patent No. 5,451,822 integrates a Pulse-Width Modulated (PWM) output of a microcomputer and amplifies the integrated PWM signal utilizing a conventional analog DC amplifier. The use of analog components to amplify the output of the microcomputer not only increases the cost of the system, but, additionally, increases its power consumption. The increase in power consumption requires larger components, which increases the physical size of the assembly and also increases the necessity for enhanced heat dissipation capabilities.

10 In published International Publication No. WO 99/14619, a plurality of electrochromic mirror elements used in an automobile are controlled by individual drive circuits for each of the electrochromic elements. The individual drive circuits are supplied with PWM signals generated by a microcomputer from photosensors positioned inside the automobile. Additionally, separate glare signals are supplied to each of the electrochromic elements such that the reflected light at a predetermined reference point is relatively constant. The system described in Publication No. WO 99/14619 multiplies the number of analog components and the increase in energy consumption resulting therefrom. Thereby, not only is the advantage derived from the generation of digital signals by the microcomputer not realized, the problems associated with analog circuitry are multiplied.

20 In United States Patent 5,675,438 complementary push-pull transistors, connected between positive and negative voltages of positive 1.6 volts DC and negative 1.6 volts DC, respectively, are operated by an oscillation circuit to supply drive pulses to an electrochromic mirror. The duty factor of the pulses are adjusted by the oscillation circuit in response to 25 light levels sensed by forward and rearward facing light sensors. Power supplies convert positive 12 volt vehicle battery voltage to the positive 1.6 volt and negative 1.6 volt levels needed to operate the push-pull transistors. The power supplies add components to the circuit and increases the power consumption thereof.

SUMMARY OF THE INVENTION

30 The present invention provides a vehicular electrochromic mirror system which significantly reduces the power consumption and thereby the component size and heat dissipation requirements thereby beneficially utilizing the digital output of a digital controller. Furthermore, the present invention provides an electrochromic vehicular mirror

system in which the interior rearview mirror assembly and exterior rearview mirror assemblies are all commonly driven off of a common drive circuit.

According to an aspect of the invention, a vehicular electrochromic rearview mirror system includes at least one electrochromic rearview assembly having an electrochromic reflective element. The electrochromic reflective element assumes a partial reflectance level in response to a signal applied thereto. The system further includes a drive circuit applying a drive signal to the electrochromic reflectance element establishing a partial reflectance level of the reflective element. The drive circuit includes a digital controller and a switching power supply controlled by the digital controller producing the drive signal.

10 In a preferred form, the switching power supply is a switching step-down converter. Most preferably, the switching power supply is a buck converter which includes an inductor and an electronic switch in electrical series connection between the electrochromic reflective element and a voltage source. The use of a switching power supply which is controlled by a digital controller greatly reduces power consumption of the individual components. This 15 may be enhanced by operating the electrical switch in a saturated condition, further reducing power consumption.

According to another aspect of the invention, a vehicle electrochromic rearview mirror system includes an interior electrochromic rearview mirror assembly and at least one exterior electrochromic rearview mirror assembly. The system further includes a plurality of 20 electrochromic reflective elements each associated with an electrochromic rearview mirror assembly. Each of the electrochromic reflective elements assumes a partial reflectance level in response to a signal applied thereto. The system further includes a drive signal that applies a drive signal to each of the electrochromic reflective elements. The drive circuit includes a digital controller, a master drive circuit responsive to the digital controller developing a first 25 drive signal applied to one of the electrochromic cells and a slave drive circuit responsive to the digital controller developing a second drive signal applied to a second of the electrochromic cells. The second drive signal is derived from the first drive signal.

Because the second drive signal is derived from the first drive signal, the reflectance level of the second of the electrochromic cells must necessarily be greater than or equal to the 30 first of the electrochromic cells. While this could reduce the flexibility of control of the reflective elements, it has been discovered that exceptional performance can be achieved along with a substantial reduction in system component count and energy usage. Preferably, the first drive signal derived from the master drive circuit is applied to one or both exterior

rearview mirrors and the second drive signal derived from the slave drive circuit is supplied to the interior rearview mirror assembly.

These and other objects, advantages, and features of this invention will become apparent upon review of the following specification in conjunction with the drawings.

5 BRIEF DESCRIPTION OF THE DRAWINGS

Fig. 1 is a side elevation of a vehicle incorporating an electrochromic mirror system according to the invention;

Fig. 2 is a side elevation of a portion of an electrochromic mirror system according to the invention;

10 Fig. 3 is a block diagram of a drive circuit according to the invention;

Fig. 4 is a block diagram of a master/slave drive circuit according to the invention;

Fig. 5 is a generalized electrical schematic diagram of the drive circuit in Fig. 3;

Fig. 6 is a detailed circuit diagram of the drive circuit in Fig. 5;

15 Fig. 7 is a diagram showing waveforms of various electrical signals generated by the drive circuit in Fig. 6;

Fig. 8 is a generalized circuit diagram of a slave drive circuit according to the invention;

20 Fig. 9 is a detailed schematic diagram of the master/slave drive circuit in Fig. 4; and

Fig. 10 is the same view as Fig. 6 of an alternative embodiment.

25 DESCRIPTION OF THE PREFERRED EMBODIMENT

Referring now specifically to the drawings, and the illustrative embodiments depicted therein, a vehicle 9 includes an electrochromic mirror system 10 having an electrochromic interior rearview mirror assembly 11 and one or more exterior rearview mirror assemblies 19 including a driver side rearview mirror assembly 19 on the driver side of the vehicle and/or a passenger side rearview mirror system on an opposite side of vehicle 9 (Fig. 1).

30 Electrochromic mirror system 10 includes an electronic control 12 which, preferably, is positioned within a housing 14 of interior rearview mirror assembly 11 (Fig. 2). Electronic control 12 includes a drive circuit 13 which receives a first input from a rearward-facing light sensor 20, which faces rearwardly in the direction of travel of the vehicle, and a second input from forward-facing light sensor 22, which faces forward with respect to the direction of travel of vehicle 9. Drive circuit 13 supplies a drive signal to electrochromic reflective element 18 included with interior rearview mirror assembly 11. It may also supply a drive signal to other electrochromic reflective elements, such as electrochromic reflective element 24 associated with exterior rearview mirror assembly 19.

In the illustrative embodiment, electronic control 12 may include a display that may be positioned behind reflective element 18 and viewed by the driver through the electrochromic reflective element as disclosed in United States Patent No. 5,285,060, issued to Larson et al., for a DISPLAY FOR AUTOMATIC REARVIEW MIRROR, the disclosure of which is incorporated herein by reference. Alternatively, the display may be positioned on a lip portion of housing 14 below reflective element 18 or on any other portion of the housing visible to the driver as illustrated in commonly assigned United States Patent No. 5,786,772, the disclosure of which is hereby incorporated herein by reference. Alternatively, the display could be in the form of a heads-up display projected from housing 14 on the vehicle 10 windshield.

Electronic control 12 may additionally include a heading sensor, or compass, which produces outputs indicative of the heading of the vehicle. Such heading sensor may be of the magneto-resistive type, such as disclosed in commonly assigned United States Patent No. 5,255,442, issued to Schierbeek et al., for a VEHICLE COMPASS WITH ELECTRONIC 15 SENSOR, or may be of the magneto-inductive type, such as disclosed in commonly assigned provisional patent application Serial No. 60/027,996, filed October 9, 1996, by Domanski for an ELECTRONIC COMPASS, the disclosures of which are hereby incorporated herein by reference, or may be of the flux-gate type, or may be of the magneto-capacitive type.

Electrochromic reflective elements 18 and 24 may be of any type such as disclosed in 20 United States Patent No. 4,902,108 issued to Byker; commonly assigned United States Patent No. 5,140,455 issued to Varaprasad et al.; commonly assigned United States Patent No. 5,910,854 issued to Varaprasad et al. and commonly assigned United States patent 25 application Serial No. 08/429,643 filed April 27, 1995, by Varaprasad et al. entitled ELECTROCHROMIC MIRRORS AND DEVICES, the disclosures of which are hereby all incorporated herein by reference. Electrochromic reflective elements 18 and 24 include electrochromic units, or cells, that decrease in transmissivity in response to a drive signal applied thereto. The decrease in transmissivity of the electrochromic unit, or cell, results in a decrease in reflectance level of the associated reflective element.

Light sensors 20 and 22 are conventional and may be applied as described in 30 United States Patent 4,917,477 issued to Bechtel et al.; United States Patent No. 3,601,614 issued to Platzer, Jr.; and commonly assigned United States Patent Nos. 4,793,690 issued to Graham et al. and 5,715,093 issued to Schierbeek et al., the disclosures of which are hereby incorporated herein by reference. Outputs of the light sensors are supplied as inputs to a microcomputer controller 26 which produces an output supplied to a switching power supply

28 (Figs. 3 and 5-7). Switching power supply 28 reduces a voltage from a voltage source 33 which, preferably, is on an order of magnitude of vehicle ignition voltage to an electrochromic drive signal. Voltage source 33 is between 5 volts DC and 18 volts DC and preferably between 8 volts DC and 12 volts DC. Most preferably, voltage source 33 is a regulated supply of approximately 8 volts DC, which requires minimum voltage reduction from ignition voltage levels of approximately 12 volt nominal, while facilitating a regulated voltage source. Alternatively, voltage source 33 can be vehicle battery voltage or ignition voltage. While use of vehicle battery/ignition voltage eliminates the requirement for even the minimal voltage regulator needed to produce a regulated source at, say, 8 volts DC, the use of an unregulated supply increases the desirability of monitoring voltage across the electrochromic reflective element as discussed in more detail below and as disclosed in commonly assigned application Serial No. 08/832,380. Drive circuit 13 supplies to electrochromic reflective element 24 a signal with a current capability of at least about 50 milliamperes, preferably of at least about 100 milliamperes and, most preferably of at least about 200 milliamperes.

For solution-phase, single-compartment, self-erasing electrochromic mirror elements commonly used commercially today, the maximum voltage tolerable for a sustained period is in the 1.0 volt to 2.0 volt range, typically less than 1.5 volts, and most typically about 1.4 volts. For solid-film electrochromic devices that utilize a layer, such as a tungsten oxide thin-filmed layer, the maximum voltage tolerable for a sustained period is in the 1.0 volt to the 3.0 volt range, typically in the 1.3 volt to 1.5 volt range. Usually, application of a voltage much in excess of such maximum tolerable voltage to the electrochromic cell for a sustained period, typically at least several seconds, may cause change to the electrochromic medium in the electrochromic cell. Electrochromic reflective element 24 can be modeled as having a series resistance shown as R_1 and a shunt resistance shown as R_2 (Fig. 5). The series resistance R_1 in the model arises from resistance of the electrodes and is on the order of 1 to 2 ohms. Shunt resistance R_2 in the model arises from internal losses of the electrochromic cell. When the cell is fully colored, the internal shunt resistance is approximately 10 ohms. The electrochromic element 24 typically develops a voltage, which is the potential stored on the cell. Upon application of external voltage thereto, the electrochromic element temporarily retains said potential even when the external voltage potential is removed and the cell is open-circuited. This property of electrochromic element 24 can be represented in the model by a capacitance C cell which is on the order of a half of farad for an interior mirror

electrochromic reflective element. The capacitance will vary as a function of the surface area of the electrochromic reflective element.

Referring now to Fig. 5, switching power supply 28 utilizes an electronic switch Q_1 in series electrical connection with an inductor L_1 between voltage source 33 and electrochromic element 24. As described above, voltage source 33 is preferably on an order of magnitude of ignition voltage. This eliminates the necessity for additional power supply to drop ignition voltage to an operating voltage level of electrochromic element 24. By eliminating the requirement for an additional power supply, a reduction in component cost and size, as well as power consumption, can be realized. Switching power supply 28 additionally includes a second switch D_1 connected between a junction 30, which is the junction between switch Q_1 and inductor L_1 , and ground. Switch D_1 is operated oppositely of switch Q_1 such that whenever switch Q_1 is open, switch D_1 is closed and *vice versa*. Switching power supply 28 preferably additionally includes a capacitor C_1 connected between a junction 32, which is a junction between inductor L_1 and cell 24, and ground. Switching power supply 28 may additionally include a third switch Q_2 which is connected in parallel with capacitor C_1 between junction 32 and ground. Switch Q_2 is for the purpose of rapidly removing charge from element 24 when it is desired to cause the reflective element to assume a high reflectance state, such as when the vehicle is placed in reverse gear and the like.

Operation of switching power supply 28 can be understood by reference to Fig. 7 in which the waveform V_{DIODE} is the voltage across switch D_1 , the waveform $I_{INDUCTOR}$ is the current through inductor L_1 and the waveform V_{CELL} is the voltage across electrochromic cell 24. Whenever switch Q_1 is closed, voltage V_{DIODE} increases to the voltage level of voltage source 33 which, in the illustrated embodiment, is approximately 8 volts DC. Whenever switch Q_1 is open, the voltage V_{DIODE} decreases to around 0 volts DC. If switch D_1 is a diode, then V_{DIODE} decreases to one diode forward drop below 0 volts which is approximately negative 0.6 volts DC. This voltage level is sufficient to cause switch D_1 to conduct if D_1 is a diode. Other schemes would suggest themselves to the skilled artisan for providing a second switch other than a diode for switch D_1 as will be described particularly in reference to the drive circuit 13" in Fig. 10. In operation, when switch Q_1 is closed, voltage level V causes a current to flow through inductor L_1 which stores energy in the inductor in addition to causing a current to flow through electrochromic cell 24. When switch Q_1 opens, the decrease in V_{DIODE} causes switch D_1 to become conducting, which conducts the current from inductor L_1 in order to allow the current in inductor L_1 to continue flowing as energy is discharged from

inductor L_1 . As can be seen from the waveform $I_{INDUCTOR}$ in Fig. 7, the current in inductor L_1 is kept flowing by the alternating opening of switches Q_1 and D_1 producing a voltage across electrochromic cell 24. For a drive signal applied to electrochromic element 24 that varies from approximately 0 volts DC to 1.5 volts DC, the ripple across electrochromic cell 24 as 5 represented by "RIPPLE" in Fig. 5 may be less than 200 millivolts and preferably less than 100 millivolts.

In the illustrative embodiment, switch Q_1 is a transistor, such as a bipolar transistor, a P channel field effect transistor, an N channel field effect transistor, or the like. Switch D_1 is a diode, but could be replaced with a bipolar transistor, a P channel field effect transistor, an 10 N channel field effect transistor, or the like. Switch Q_2 may be a bipolar transistor, P channel or N channel field effect transistor, or the like. Capacitor C_1 is for the purpose of reducing ripple on the voltage applied to electrochromic cell 24. While the capacitance C_{CELL} of the model of electrochromic cell 24 is substantially larger than the capacitance of capacitor C_1 , capacitor C_1 avoids undesirable voltage spikes being applied to electrochromic cell 24 as a 15 result of series resistor R_1 .

A detailed embodiment of drive circuit 13 is set forth in Fig. 6. A microcomputer controller 26 has an output 34 which supplies a Pulse-Width Modulated (PWM) signal through a resistor R_{44} to the base of a PNP bipolar transistor Q_1 . It should be understood that Q_1 transistors could alternatively be an NPN transistor or MOSFET with suitable adjustments 20 to circuit configuration. Transistor Q_1 is supplied through a 22 ohm resistor R_{51} to a voltage source 33 which may be in the range of from 5 volts DC to 16 volts DC and preferably between 8 volts and 12 volts and most preferably approximately 8 volts DC. Transistor Q_1 is in electrical series connection with inductor L_2 between voltage source 33 and electrochromic cell 24. The purpose of series resistor R_{51} is in order to reduce any over-voltage condition 25 on electrochromic cell 24 should transistor Q_1 remain closed for any reason. Diode D_1 is connected between junction 30 and ground. Microcomputer 26 has a second output 36 which is supplied through a resistor R_{58} to the base of an NPN transistor Q_2 which is connected across the terminals T_1 and T_2 of electrochromic element 24. Microcomputer 26 activates 30 output 36 in order to rapidly dissipate voltage across electrochromic reflective element 24 thereby rapidly driving the reflective element to a high reflectance level. Microcontroller 26 has an input 68 which is connected through a resistor R_{62} to terminal T_1 of electrochromic reflective element 24. The purpose of input 68 is to allow microcomputer 26 to monitor the voltage across electrochromic reflective element 24, which is proportional to the light

transmittance of electrochromic reflective element 24, thereby providing an indication of the reflectance level of the exterior electrochromic mirror assembly 19.

In operation, microcomputer 26 samples light levels sensed by light sensors 20, 22 and supplies a PWM signal on its output 34 sufficient to alternately drive transistor Q_1 between a saturated state and an open-circuit state. The proportion in time of the PWM signal outputted by microcomputer 26 on output 34 that Q_1 is saturated varies in duty cycle from approximately 0 percent to 65 percent, preferably from 0 percent to 50 percent and most preferably from 0 percent to 30 percent. The higher the portion of the duty cycle during which Q_1 is saturated, the higher voltage supplied to electrochromic cell 24. The repetition rate of the signal supplied to output 34 is preferably above the allowable range of human hearing, such as 15 kilohertz or higher. Microcomputer controller 26 periodically samples the voltage on input 68 in order to be apprised of the voltage across the electrochromic element which is indicative of the reflectance level of the corresponding rearview mirror assembly. Preferably, the periodic voltage sampling is carried out at a consistent portion of the waveform applied to electrochromic element 24 in order to avoid oscillation in the feedback loop. Most preferably, the voltage sample is taken during a period when transistor Q_1 is not conducting. This provides a more accurate determination of the residual charge on the electrochromic cell. As disclosed in commonly assigned patent application Serial No. 08/832,380, filed on April 2, 1997, the disclosure of which is hereby incorporated herein by reference, microcomputer 26 may additionally supply a voltage to electrochromic cell 24 that is temporarily higher than permissible prolonged coloration voltage levels in order to rapidly change the transmittance of electrochromic cell 24 such as when a vehicle with bright headlights pulls up behind vehicle 9. Under such conditions, microcomputer 26 is able to ensure that the over voltage supplied to electrochromic cell 24 is kept within a safe level by monitoring input 68.

However, it should be understood that even during normal operating conditions, or steady-state conditions, in which electrochromic cell 24 is not being driven in an over voltage condition, it may still be desirable to monitor the voltage across electrochromic cell 24 to endeavor that the desired reflectance level calculated by microcomputer 26 in response to inputs received from sensors 20, 22 corresponds with the actual reflectance level produced by switching power supply 28. For example, monitoring of the voltage across the electrochromic reflective element allows the same drive circuit to be able to drive a wide variety of reflective element surface areas. For example, an electrochromic reflective element utilized as an interior mirror of a vehicle may have a surface area in the range of 90

cm² to 150 cm² and typically in the range of 110 cm² to 130 cm². A steady state steady-state reflectance level current draw, after color transitions have settled, is typically in the range of between 60 milliamperes and 180 milliamperes with a range of 80 milliamperes to 150 milliamperes being typical. Exterior rearview mirrors can be even larger with a surface area 5 of approximately 350 cm², and greater, with a commensurate increase in current.

Furthermore, in cases where the drive circuit is driven directly from an unregulated supply, such as vehicle battery or ignition voltage, monitoring voltage across the cell facilitates a more stable and accurate drive signal being applied to the electrochromic reflective element notwithstanding significant variation in supply voltage.

10 Furthermore, temperature variation experienced in the mirror assembly, especially exterior mirror assemblies, can significantly alter the current of the drive signal needed to achieve a particular voltage and, hence, the desired reflectance level. By monitoring voltage level across the electrochromic reflectance element, drive circuit 13 can accommodate such variation in surface area, temperature, and the like. It may also be advantageous to provide 15 an input from a temperature sensor to microcomputer 26 indicative of the environment the electrochromic reflective element is operating in. This is especially relevant in the case of exterior electrochromic rearview mirror assembly 19, which is exposed to a wide range of temperature. However, temperature monitoring may also be used with the interior 20 electrochromic rearview mirror assembly 11. Microcomputer 26 may use the information on the temperature environment of the electrochromic reflective element to enhance the monitoring of the voltage across the electrochromic reflective element and producing a 25 resulting voltage across the reflective element, such as by varying the duty cycle of the switching power supply. This is because the relationship between the voltage across the reflective element and its reflectivity is non-linear, with the non-linearity related to the temperature environment of the electrochromic reflective element.

Although switching power supply 28 is illustrated as a buck converter made up of individual discrete components, other configurations are possible. The individual components of the buck converter could be combined into an integrated circuit package of the type which is commercially available. Additionally, the inductor could be replaced with a 30 resistor. Although energy efficiency would be reduced, the reduction may be acceptable especially in view of the cost reduction realized by eliminating a rather costly component, the inductor. Instead of adjusting the voltage level of the output of buck converter 28 by varying the duty cycle of a PWM signal applied to its input, any type of waveform, such as a DC voltage level, from the microcomputer to the switching power supply may be varied in order

to control switching power supply 28. Alternatively, a PWM pulse signal produced by the microcomputer could be integrated prior to being supplied as an input to such alternative switching power supply. Other suitable means for signaling the desired output of the switching power supply may be used.

5 Because transistors Q_1 and diode D_6 are operated in a saturated mode, exceptional energy utilization efficiency is realized. In the illustrated embodiment, switching power supply, or switching step-down converter 28, operates at an efficiency level of at least 75 percent and may achieve an 85 or even 95 percent energy efficiency. Because a high energy efficiency reduces the amount of energy which must be removed as waste heat, the
10 component size and heat dissipation requirements are substantially reduced.

In an alternative electrochromic mirror system for a vehicle, an electronic control has a drive circuit 13' including a master drive circuit 40 and a slave drive circuit 42 (Figs. 4, 8 and 9). Master drive circuit 40, which is illustrated in Fig. 8 as a voltage source, produces a drive signal at its output 32 which may be supplied to an electrochromic reflective element
15 24. Slave drive circuit 42 derives from output 32 a second signal which is supplied to another rearview mirror electrochromic reflective element 18. In the illustrated embodiment, master drive circuit may be of the type illustrated in Figs. 5 and 6, although other commonly available drive circuits, such as the type disclosed in Bechtel et al. United States Patent 5,451,822, could be utilized. Slave drive circuit 42 includes a first electronic switch Q_3 in
20 electrical series connection between master drive circuit 40 and electrochromic reflective element 18 and a second electronic switch Q_4 which shunts electrochromic reflective element 18 when closed. Electronic switch Q_4 is for the purpose of rapidly discharging accumulated charge on electrochromic reflective element 18 when it is desired to rapidly increase the reflectivity of the associated reflective element such as when the vehicle is replaced in
25 reverse gear.

Electronic switch Q_3 is opened and closed in a PWM fashion in order to supply a pulsed DC voltage to slave driven electrochromic reflective element 18 that is derived from the drive signal produced by the master drive circuit 32. The drive signal supplied to slave electrochromic reflective element 18 is at a level that is less than or equal to the master drive
30 signal at 32. If transistor Q_3 is closed 100 percent of the time, then the slave drive signal supplied to slave electrochromic element 18 will be equal to the master drive signal supplied to master cell 24. As the duty cycle of electronic switch Q_3 being in a saturated state decreases below 100 percent, the slave voltage supplied to electrochromic element 18 will decrease as a proportion of master drive signal 44 supplied to electrochromic element 24. In

the illustrated embodiment, switch Q_3 is operated at a duty cycle that is greater than or equal to 65 percent and preferably in a range of between 75 percent and 100 percent.

Advantageously, transistor Q_3 can be operated at a relatively slow repetition rate without producing undesirable ripple in the voltage supplied to slave electrochromic element 18. This is accomplished by utilizing the significant capacitance C_{CELL} of electrochromic element 18.

Preferably, electronic switch Q_3 is operated at a pulse repetition rate that is at the low end of the audio perception of humans, but above a rate that may produce noticeable flicker; namely, below 200 hertz and preferably less than or equal to 100 hertz. Most preferably, the pulse repetition rate of electronic switch Q_3 is approximately 50 hertz. In addition to allowing the repetition rate of electronic switch Q_3 to be relatively low, the large capacitance of electrochromic cell 15 allows the change in reflectance level thereof to be changed in relatively coarse steps. For example, the change in duty cycle of electronic switch Q_3 could be changed in 5 percent steps, 10 percent steps, or even 15 percent steps. This results in a lessening of the software requirement for controlling the slave drive. Alternatively, switches Q_1 and D_1 of master drive 40 could operate at a repetition rate below human audible reception and switches Q_4 of slave drive 42 operate at a rate above human audible perception.

In the illustrated embodiment, slave drive circuit 42 supplies a drive signal to electrochromic element 18 that is part of interior electrochromic mirror assembly 11 and master drive circuit 40 supplies a drive signal to electrochromic element 24 that is a part of exterior electrochromic rearview mirror assembly 19 of which two are preferred on a vehicle. This is based upon a preference that the reflectance level of the interior rearview mirror should be no less than the reflectance level of the exterior rearview mirrors. This arrangement is particularly advantageous in providing a relationship between the reflectivity of the interior rearview mirror assembly and exterior rearview mirror assemblies that takes into account the configuration of the vehicle as disclosed in commonly assigned provisional patent applications Serial No. 60/103,721 filed on October 9, 1998, by Kenneth Schofield and Kenneth L. Schierbeek, and Serial No. 60/117,633 filed on January 28, 1999, by Kenneth Schofield and Kenneth L. Schierbeek, the disclosures of which are hereby incorporated herein by reference. However, it may be desirable for particular applications to drive the interior electrochromic reflective element with the master drive circuit and one or both exterior electrochromic reflective elements with the slave drive circuit. Although a master/slave drive circuit according to the invention has more design constraints than other known electrochromic mirror systems, which provide independent drive signals for each mirror controlled, it has been discovered that any differences are not perceived by the driver.

A detailed embodiment of drive circuit 13' is illustrated in Fig. 9 in which an inductor L₄ and capacitor C₄ filter noise for a voltage source 33 to the emitter of transistor Q₁. A diode D₂ is connected from output 34 of microcomputer 26 to a voltage source, such as 5 volts. The purpose of diode D₂ is to clip any voltage spikes that may be fed to line 34 such as 5 from inductor L₆. Otherwise, the master drive of drive circuit 13' is identical with switching power supply 28 in Fig. 6. Microcomputer 26 has an output 46 which supplies a PWM drive signal to an NPN transistor Q₃. The emitter of transistor Q₃ is supplied at 48 to a terminal of slave electrochromic element 18. Transistor Q₃ selectively interconnects line 48, which supplies a drive signal to slave electrochromic element 18, with line 32 which supplies a 10 drive signal to master electrochromic cell 24. Transistor Q₃ is selectively switched between an open state and a saturated state in response to an output 46 from microcomputer 26. By varying the duty cycle of the PWM signal at output 46, microcontroller 26 selectively adjusts the relative proportion between the drive signal supplied to slave electrochromic element 18 and the drive signal supplied to master electrochromic element 24. Microcomputer 26 15 additionally has an output 50 which is supplied to a base of a transistor Q₄. Transistor Q₄ is connected between line 48 and ground. When microcomputer 26 switches transistor Q₄ to a saturated state, electrical charge on slave electrochromic reflective element 18 is rapidly dissipated. Transistors Q₃ and Q₄ are placed in saturation at different times. Microcomputer 26 additionally receives an input at 52 which is connected through a resistor R29 to line 48. 20 Input 52 supplies to microcomputer 26 a signal representative of the voltage across slave electrochromic element 18.

In operation, microcomputer 26 determines a desired reflectance level for interior electrochromic rearview mirror assembly 11 and electrochromic exterior mirror assembly 19. The values of desired reflectance level may be established, by way of example, from the 25 respective light levels sensed by light sensors 20, 22 as well as the configuration of the vehicle as set forth in commonly assigned provisional patent application Serial No. 60/117,633, filed by Kenneth Schofield et al. on January 28, 1999, the disclosure of which is hereby incorporated herein by reference. The microcomputer controller then selects a duty cycle for the PWM signal supplied at output 34 to transistor Q₁. The selected duty cycle may 30 take into account, by way of example, the voltage across master electrochromic cell 24 as monitored by input 38 from line 44 and the degree of change in reflectance level desired. The PWM signal supplied at output 34 produces a voltage level at line 32 which is monitored by the microcomputer at input 38. If master electrochromic element 24 is being switched to a substantially lower transmissivity, it may be desirable to momentarily increase the voltage

supplied to the master electrochromic cell in order to rapidly modify the transmissivity of the cell. During such transitions, microcomputer 38 may more frequently monitor the voltage at line 44 in order to ensure that any temporary over voltage condition of master electrochromic element 24 is kept within a prescribed maximum voltage level. Values of maximum voltage 5 level, as well as PWM duty cycles, may be selected according to the principles disclosed in commonly assigned application Serial No. 08/832,380, entitled DIGITAL ELECTROCHROMIC MIRROR SYSTEM, filed on April 2, 1997, by Kenneth L. Schierbeek, the disclosure of which is hereby incorporated herein by reference. If microcomputer controller 26 determines that the reflectance level of master electrochromic 10 element 24 should be substantially increased, then the PWM signal would be disconnected from output 34 and output 36 energized in order to place transistor Q_2 in a saturated condition. This will rapidly dissipate charge on master electrochromic element 24 to increase the transmissivity thereof.

Microcomputer controller 26 additionally selects a duty cycle for a PWM signal 15 supplied at output 46. The PWM signal will be selected at least as a function of the desired reflectance level for slave electrochromic element 18. The selected duty cycle may take into account the current reflectance level of master electrochromic element 24 represented at least in part by the voltage across slave electrochromic element 18 on line 48, as monitored by input 52 to the microcomputer. Once a duty cycle is selected, microcomputer 26 produces a 20 PWM signal at output 46. Microcomputer 26 may choose to dwell on a particular duty cycle at output 46 unless the calculated duty cycle change is more than a given percent. In the illustrated embodiment, the duty cycle is not changed by microcomputer 26 unless the calculated duty cycle change is at least 5 percent, more preferably at least 10 percent and most preferably at least 15 percent. If the calculated duty cycle of slave electrochromic 25 reflective element 18 results in a substantial increase in the transmissivity of reflective element 18, then microcomputer 26 removes the PWM signal from output 46 and supplies an output at 50 to cause transistor Q_4 to enter a saturated mode which quickly dissipates any charge from line 48 thereby rapidly increasing the transmissivity of slave electrochromic element 18.

30 Drive signal 48 applied to interior electrochromic reflective element 18 is compensated for the thermal environment of interior electrochromic mirror assembly 11 as well as the surface area, present reflectance level, and the like, of interior electrochromic reflective element 18. This is accomplished by drive circuit 13' monitoring voltage across electrochromic reflective element 18 as supplied to input 52 of microcomputer 26. Drive

signal 32 applied to exterior electrochromic reflective element 24 is compensated for the thermal environment of exterior electrochromic mirror assembly 19, as well as the surface area, present reflectance level, and the like, of exterior electrochromic reflective element 24. This is accomplished by drive circuit 13' monitoring voltage across electrochromic reflective element 24 as supplied to input 38 of microcomputer 26. In this manner, drive circuit 13 can compensate for different thermal environments of interior and exterior electrochromic mirror assemblies 11, 19. Additionally, microcomputer 26 may receive temperature sensor inputs for any of the interior and/or electrochromic exterior rearview mirror assemblies to further compensate for different thermal environments. This additional input or inputs may assist microcomputer 26 in reducing effects created by non-linearity in the relationship between voltage across the electrochromic reflective element and its reflectance level for different temperatures.

Although transistor Q₃ is illustrated as operated in a saturated mode at a particular PWM, the slave drive could alternatively be a transistor drive in a non-saturated state either continuously or at a PWM.

The present invention, although illustrated with one master electrochromic reflective element and one slave electrochromic reflective element, could be utilized with two master drive circuits to drive two electrochromic reflective elements, such as for two exterior rearview mirror assemblies and one slave electrochromic element which is slave to one of the master drive circuits. Alternatively, an electrochromic mirror system may utilize one master electrochromic element to drive one exterior electrochromic rearview mirror and two slave electrochromic elements, each driven to an independently or tandemly controlled partial reflectance level that is higher than the partial reflectance level of the master rearview assembly. Other variations may suggest themselves to the skilled artisan.

In yet a further embodiment, a drive circuit 13'' utilizes a transistor Q₆ to replace both the functions of diode D₁ and bleaching transistor Q₂ (Fig. 10). As will be observed from Fig. 10, microcontroller 26 includes a single output 34 which is supplied to the base of a transistor Q₅ whose collector is connected to a junction 100 which is supplied through a resistor 102 to the base of transistor Q₁ and to resistors 104 and 106 to the base of transistor Q₆. When microcomputer 26 drives transistor Q₅ into saturation, transistor Q₁ is switched into saturation, and Q₆ is cut off. During such portion of the cycle, energy is stored in inductor L₂ and a current is forced through electrochromic reflective element 24. When microcomputer controller 26 switches transistor Q₅ into an open circuit condition, transistor Q₁ is switched into an open circuit condition and transistor Q₆ is switched into saturation.

This causes a current to continue to flow through inductor L_2 and electrochromic reflective element 24 as the energy from inductor L_2 is dissipated. As microcomputer controller 26 switches transistor Q_5 according to a rectangular wave, electrochromic reflective element 24 is provided with a drive signal whose voltage is proportional to the duty cycle of such 5 rectangular wave. If it is decided by microcomputer controller 26 to rapidly increase the reflectivity of electrochromic reflective element 24 by rapidly dissipating the charge accumulated thereon, microcomputer 26 holds output 34 in a low state which causes transistor Q_5 to be open-circuited and transistor Q_6 to be driven to saturation. This rapidly dissipates any charge on electrochromic reflective element 24. When microcomputer 10 controller 26 once again determines the need to decrease the reflectivity of electrochromic reflective element 24, it applies a rectangular waveform to its output 34, thereby applying a drive signal as previously described. By suitable circuit modifications, transistors Q_5 and Q_6 can each be bipolar transistors, N channel field effect transistors, P channel field effect transistors, or the like.

15 The functions performed by microcomputer 26 could be implemented by any circuit capable of generating a digital signal including a programmable computer, a dedicated digital logic circuit, or the like. Preferably, the components making up electronic control 13, 13', 13'' are commonly mounted in housing 14 of interior electrochromic mirror assembly 11 as disclosed in commonly assigned United States Patent No. 5,424,898, the disclosure of which 20 is hereby incorporated herein by reference. However, it should be understood that the components could alternatively be located in exterior electrochromic mirror assembly 19 or in a location separate from the mirror assemblies as disclosed in commonly assigned United States Patent No. 5,659,423, the disclosure of which is hereby incorporated herein by reference.

25 Changes and modifications in the specifically described embodiments can be carried out without departing from the principles of the invention which is intended to be limited only by the scope of the appended claims, as interpreted according to the principles of patent law including the doctrine of equivalents.